**Indian National Academy of Engineering (INAE)**

**MOS-AK/GSA India 2012:**

**International Workshop on Device Modelling for Microsystems**

16–17 March 2012

Jaypee Institute of Information Technology (JIIT), A-10, Sector-62, Noida (U.P.)

MOS-AK/GSA is a global organization/forum that discusses and shares all information related to the compact modelling at all levels of the device and circuit characterization, modelling and simulations. This organization brings people in the compact modelling field together and helps networking among them, enhance open platform for information exchange related to compact/Spice modelling and Verilog-A standardization as well as obtain feedback from technology developers, circuit designers, and CAD tool vendors. The topics cover all important aspects of compact model development, implementation, deployment and standardization within the main theme – frontiers of the compact modelling for CMOS/SOI circuit simulation and nm-scale MEMS designs.

MOS-AK/GSA India 2012 event, being organized by us for the first time in India, will also discuss the frontiers of device modelling for Microsystems with emphasis on simulation-aware models. The specific workshop goal will be to classify the most important directions for the future development of the microelectronic device models, not limiting the discussion to compact models, but including physical, analytical and numerical models, to clearly identify areas that need further research and possible contact points between the different modelling domains. This workshop is designed for device process engineers (CMOS, SOI, BiCMOS, SiGe) who are interested in device modelling; ICs designers (RF/Analog/Mixed-Signal/SoC) and those starting in that area as well as device characterization, modelling and parameter extraction engineers. The content will be beneficial for anyone who needs to learn what is really behind the IC simulation in modern device models.

The workshop is also envisaged to serve as a platform for launch of MOS-AK/GSA India which forum will help galvanize the microelectronics modelling community in India which exists as strong pockets over various Universities, Institutes of Technology and Industries.

Concerned/interested professionals are welcome to participate in this Workshop. Submission of poster papers is also welcome. Details of the workshop can be viewed at [http://inae.org/seminar.htm](http://inae.org/seminar.htm).

For further details please contact: Mrs Pratigya Laur, Indian National Academy of Engineering (INAE), Ph: +91-11-2658 2635; e-mail: inaehq@gmail.com

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**Cytogenetics Laboratory, Department of Zoology**

Banaras Hindu University

**Research Positions**

Applications are invited for following research positions:

1. **Research Associate/Post-doctoral Fellow:** Under the DAE-Raja Ramanna Fellowship (up to 30 June 2014), remuneration as per qualifications prescribed by the DAE.

2. **Junior Research Fellow @ Rs 16,000.00 p.m.** (plus HRA as admissible) in a DBT sponsored multi-institutional project entitled ‘A drosophila model for unraveling the link between cellular stress response, carcinogenesis and anti-cancer drug screening’ (up to November 2014).

The posts are temporary and co-terminus with the respective project. Research would involve molecular cell biological and gene expression studies using Drosophila model. Candidates for RA/PDF should hold a Ph.D. degree (RA) or should have submitted thesis (PDF) in any branch of life sciences with experience in Genetics/Molecular Biology/Biochemistry and with consistently good academic career. Applicants for JRF should have a consistently good academic career with M.Sc. (at least 55% marks) in Zoology/Genetics/Molecular and Human Genetics/Biochemistry/Biotechnology/Life Sciences and should have preferably qualified CSIR-UGC NET or GATE. Upper age limit, 45 years for RA and 28 years for JRF, is relaxable by 5 years for SC/ST/physically handicapped/female candidates. Candidates should have good knowledge of basic Genetics and Cell Biology. All things being equal, SC/ST candidates will be preferred as per GOI rules.

Application on plain paper giving name, permanent and correspondence address, names of father and mother, telephone no. and e-mail address (if available), details of educational career (starting from High School or equivalent) along with attested copies of all mark-sheets and certificates, two sealed confidential reference letters, and details of any research or other experience, etc. should reach Prof. S. C. Lakhotia, Professor Emeritus and DAE-Raja Ramanna Fellow, Department of Zoology, Banaras Hindu University, Varanasi 221 005 within 21 days of this advertisement.

No TA/DA will be payable, if called for interview.